Dell EMC PMem 200 Series

User's Guide

Notes, cautions, and warnings

(i) NOTE: A NOTE indicates important information that helps you make better use of your product.

CAUTION: A CAUTION indicates either potential damage to hardware or loss of data and tells you how to avoid the problem.

MARNING: A WARNING indicates a potential for property damage, personal injury, or death.

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Introduction

Dell EMC offers Intel Optane persistent memory (PMem) 200 series, a nonvolatile memory that has the same form factor as a standard DDR4 DIMM.

PMems come in three capacities: 128 GB, 256 GB, and 512 GB.

PMems are installed in the memory slots of the server and are compatible with RDIMMs and LRDIMMs.

PMem can be configured in two modes:

- Memory mode
- App-direct mode

In Memory mode, PMems act as volatile system memory and any RDIMMs or LRDIMMs installed operate as cache for the PMems.

In App-direct mode, PMems operate as byte addressable memory mapped persistent memory. PMems and DRAM act as independent memory resources. RDIMMs or LRDIMMs acts as volatile system memory. Applications can access persistent memory by using memory load or store commands. Legacy applications that access storage as block devices can access persistent memory (PMem) through the PMem block driver.

PMems do not require an extra power supply or battery and are inherently persistent.

This document is intended to help customers understand the basics of Intel's PMem technology integrated in Dell EMC PowerEdge systems. It covers the fundamentals of installation and configuration of key settings for both the operating modes.

Topics:

- System requirements
- Terminology

System requirements

Table 1. System requirements

Component	Minimum version required
System	R650, R750, R750xa, MX750c, XR11, XR12
Processor	3 rd Generation Intel Xeon Platinum or Gold processors
BIOS	R650 - 1.1.1
	R750 - 1.1.1
	R750xa - 1.1.1
	MX750c - 1.1.2
	XR11, XR12 - 1.0.2
CPLD	R650 - 1.0.1
	R750 - 1.0.1
	R750xa - 1.0.1
	MX750c - 1.0.1
	XR11, XR12 - 1.0.0
iDRAC	4.40.20.0
PMem firmware	02.02.00.1553

Table 1. System requirements (continued)

Component	Minimum version required
OS	Microsoft Windows Server 2019
	VMware ESXi 7.0 Update 2
	Red Hat Enterprise Linux 8.2
	SUSE Linux Enterprise Server 15 SP2

Terminology

Table 2. Terminology

Terminology	Description
App-direct mode (AD)	Persistent memory is accessed directly by applications as byte-addressable memory.
CLI	Intel command-line interface in UEFI or Linux environment
PMem	Intel Optane persistent memory
GiB	Gibibyte
	1 GiB = 1024 MiB
GB	Gigabyte
	1 GB = 1000 MB
iMC	Integrated Memory Controller
Interleave Set	Contiguous App-direct mode capacity that is created by interleaving the persistent capacity of one or more PMems.
x1 (by one) Interleave	Interleave set that includes App-direct capacity from only one PMem pronounced "by one." This is essentially "Noninterleaved"
PM, PMem	Persistent Memory
Memory Mode (MM)	RDIMM or LRDIMM becomes cache for nonpersistent PMems.
MiB	Mibibyte
	1 MiB = 1024 KB = 1048576 bytes
МВ	Megabyte
	1 MB = 1000 KB = 1000000 bytes
Namespace	A persistent memory device that is made available in the file system.
TiB	Tebibyte
	1 TiB = 1024 GiB
ТВ	Terabyte
	1 TB = 1000 GB

Change list

Table 3. Change list

Version	Changes		
A00	Original Version		
A01	 Removed Table 11: Processor type and maximum memory limits. Added to Table 9: 256GB LRDIMM + 512GB BPS configuration. 		

Hardware

Topics:

- Server hardware configuration
- DIMM installation and removal
- PMem hardware configuration

Server hardware configuration

PMem is supported in R650, R750, R750xa, MX750c, XR11 and XR12 PowerEdge servers with third-generation Intel Xeon Scalable Processors of Gold and Platinum grade. See PMem configurations for a list of fully supported and validated PMem configurations in two-socket servers. Four-socket configurations are a direct scale up of two-socket configurations.

NOTE: For PowerEdge R750 or R750xa, 1400 W or 2400 W PSUs are required when Intel PMems are present in the system. If these guidelines are not followed, the system may not have sufficient power holdup in an AC loss scenario to flush in-flight data contents to persistent media. All systems built from Dell Technologies factories will be pre-configured to meet these guidelines.

DIMM installation and removal

Industry standard DIMM practices and procedures must be followed while handling, installing, or removing PMem memories.

For more details on procedures for installing or removing modules, see the *Standard Practices and Procedures - Module Insertion Procedure for DIMM and miniDIMM Connectors* documentation by JEDEC.

JEDEC Standards (www.jedec.org): Document Number SPP-023B.

PMem hardware configuration

PMem recommended topologies

This section contains the high-level introduction on PMem configuration and provisioning concepts.

The following topologies are recommended per processor socket. For multiple socket systems with more than one PMem, each socket should be populated identically.

See the Installation and Service Manual for memory installation guidelines of respective servers.

NOTE: The following image and table is for reference which displays the PowerEdge R750 processor and DIMM slot locations.

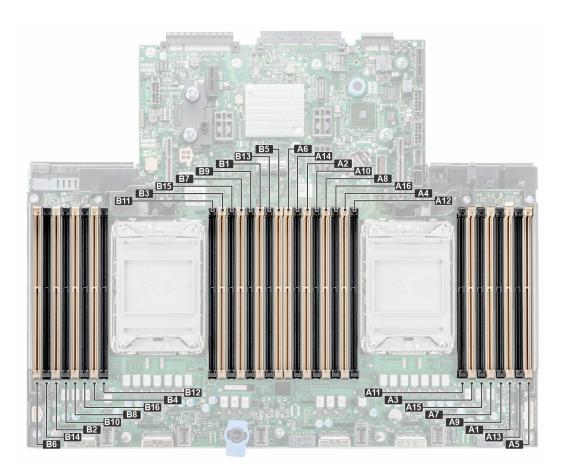


Figure 1. Memory layout for the PowerEdge R750

Table 4. Supported Intel Persistent Memory 200 series for dual processor configurations

Configuration	Description per	Memory population rules			
	processor	RDIMMs or LRDIMMs	Intel Persistent Memory 200 series		
Configuration 1	4 x RDIMMs, 4 x Intel Persistent Memory 200 series	Processor1 {A1, 2, 3, 4} Processor2 {B1, 2, 3, 4}	Processor1 {A5, 6, 7, 8} Processor2 {B5, 6, 7, 8}		
Configuration 2	6 x RDIMMs, 1 x Intel Persistent Memory 200 series	Processor1 {A1, 2, 3, 4, 5, 6} Processor2 {B1, 2, 3, 4, 5, 6}	Processor1 (A7) Processor2 (B7)		
Configuration 3	8 x RDIMMs, 1 x Intel Persistent Memory 200 series	Processor1 {A1, 2, 3, 4, 5, 6, 7, 8} Processor2 {B1, 2, 3, 4, 5, 6, 7, 8}	Processor1 (A9) Processor2 (B9)		
Configuration 4	8 x RDIMMs, 4 x Intel Persistent Memory 200 series	Processor1 {A1, 2, 3, 4, 5, 6, 7, 8} Processor2 {B1, 2, 3, 4, 5, 6, 7, 8}	Processor1 {A9, 10, 11, 12} Processor2 {B9, 10, 11, 12}		
Configuration 5	8 x RDIMMs, 8 x Intel Persistent Memory 200 series	Processor1 {A1, 2, 3, 4, 5, 6, 7, 8} Processor2 {B1, 2, 3, 4, 5, 6, 7, 8}	Processor1 (A9, 10, 11, 12, 13, 14, 15, 16) Processor2 (B9, 10, 11, 12, 13, 14, 15, 16)		

Table 4. Supported Intel Persistent Memory 200 series for dual processor configurations (continued)

Configuration	Description per	Memory population rules			
	processor	RDIMMs or LRDIMMs	Intel Persistent Memory 200 series		
Configuration 6	12 x RDIMMs, 2 x Intel Persistent Memory 200 series	Processor1 {A1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 15, 16} Processor2 {B1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 15, 16}	Processor1 (A5, 6) Processor2 (B5, 6)		

i NOTE: There are limited configurations available for dual socket servers with only one processor populated.

Table 5. Intel Persistent Memory 200 series Configuration 1 - 4 x RDIMMs/ LRDIMMs, 4 x Intel Persistent Memory 200 series per processor

Total No of RDIMMs/ LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
4	4	16	128	64	512	MM or AD
4	4	32	128	128	512	MM or AD
4	4	64	128	256	512	AD
4	4	128	128	512	512	AD
4	4	256	128	1024	512	AD
4	4	16	256	64	1024	MM or AD
4	4	32	256	128	1024	MM or AD
4	4	64	256	256	1024	MM or AD
4	4	128	256	512	1024	AD
4	4	256	256	1024	1024	AD
4	4	16	512	64	2048	AD
4	4	32	512	128	2048	MM or AD
4	4	64	512	256	2048	MM or AD
4	4	128	512	512	2048	MM or AD

Table 6. Intel Persistent Memory 200 series Configuration 2 - $6 \times RDIMMs/LRDIMMs$, $1 \times Intel Persistent Memory 200 series per processor$

Total No of RDIMMs/ LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
6	1	16	128	96	128	AD
6	1	32	128	192	128	AD
6	1	64	128	384	128	AD
6	1	128	128	768	128	AD
6	1	16	256	96	256	AD
6	1	32	256	192	256	AD

Table 6. Intel Persistent Memory 200 series Configuration 2 - 6 x RDIMMs/ LRDIMMs, 1 x Intel Persistent Memory 200 series per processor (continued)

Total No of RDIMMs/ LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
6	1	64	256	384	256	AD
6	1	128	256	768	256	AD
6	1	256	256	1536	256	AD
6	1	16	512	96	512	AD
6	1	32	512	192	512	AD
6	1	64	512	384	512	AD
6	1	128	512	768	512	AD

Table 7. Intel Persistent Memory 200 series Configuration 3 - 8 x RDIMMs/ LRDIMMs, 1 x Intel Persistent Memory 200 series per processor

Total No of RDIMMs / LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
8	1	16	128	128	128	AD
8	1	32	128	256	128	AD
8	1	64	128	512	128	AD
8	1	128	128	1024	128	AD
8	1	16	256	128	256	AD
8	1	32	256	256	256	AD
8	1	64	256	512	256	AD
8	1	128	256	1024	256	AD
8	1	256	256	2048	256	AD
8	1	16	512	128	512	AD
8	1	32	512	256	512	AD
8	1	64	512	512	512	AD
8	1	128	512	1024	512	AD

Table 8. Intel Persistent Memory 200 series Configuration 4 - 8 x RDIMMs/ LRDIMMs, 4 x Intel Persistent Memory 200 series per processor

Total No of RDIMMs / LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
8	4	16	128	128	512	MM or AD
8	4	32	128	256	512	AD
8	4	64	128	512	512	AD

Table 8. Intel Persistent Memory 200 series Configuration 4 - 8 x RDIMMs/ LRDIMMs, 4 x Intel Persistent Memory 200 series per processor (continued)

Total No of RDIMMs / LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
8	4	128	128	1024	512	AD
8	4	16	256	128	1024	MM or AD
8	4	32	256	256	1024	MM or AD
8	4	64	256	512	1024	AD
8	4	128	256	1024	1024	AD
8	4	256	256	2048	1024	AD
8	4	16	512	128	2048	MM or AD
8	4	32	512	256	2048	MM or AD
8	4	64	512	512	2048	MM or AD
8	4	128	512	1024	2048	AD

Table 9. Intel Persistent Memory 200 series Configuration $5 - 8 \times RDIMMs / LRDIMMs$, $8 \times Intel Persistent Memory 200 series per processor$

Total No of RDIMMs / LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
8	8	16	128	128	1024	MM or AD
8	8	32	128	256	1024	MM or AD
8	8	64	128	512	1024	AD
8	8	128	128	1024	1024	AD
8	8	16	256	128	2048	MM or AD
8	8	32	256	256	2048	MM or AD
8	8	64	256	512	2048	MM or AD
8	8	128	256	1024	2048	AD
8	8	256	256	2048	2048	AD
8	8	16	512	128	4096	AD
8	8	32	512	256	4096	MM or AD
8	8	64	512	512	4096	MM or AD
8	8	128	512	1024	4096	MM or AD
8	8	256	512	2048	4096	AD

Table 10. Intel Persistent Memory 200 series Configuration 6 - 12 x RDIMMs/ LRDIMMs, 2 x Intel Persistent Memory 200 series per processor

Total No of RDIMMs / LRDIMMs	Total No of Intel Persistent Memory 200 series DIMMs	1 R/LRDIMM capacity (GB)	1 Intel Persistent Memory 200 series capacity (GB)	Total Standard Memory Capacity (GB)	Total PM Capacity (GB)	Supported Modes
12	2	16	128	192	256	AD
12	2	32	128	384	256	AD
12	2	64	128	768	256	AD
12	2	128	128	1536	256	AD
12	2	16	256	192	512	AD
12	2	32	256	384	512	AD
12	2	64	256	768	512	AD
12	2	128	256	1536	512	AD
12	2	16	512	192	1024	AD
12	2	32	512	384	1024	AD
12	2	64	512	768	1024	AD
12	2	128	512	1536	1024	AD

PMem mixing and population rules

This section has general rules for DIMM mixing and population.

Each system must contain only one capacity of PMem. If you mix PMem capacities, an F1/F2 warning message is displayed. This is not a supported configuration and must not be populated. The table PMem configurations replace the following rules:

Mixing rules

- PMem can be mixed with RDIMM, LRDIMM, and 3DS LRDIMM.
- Mixing DDR4 DIMM types (RDIMM, LRDIMM, 3DS LRDIMM), within a channel, iMC, socket, or across sockets are not supported.
- x4 and x8 DDR4 DIMMs can be mixed within a channel.
- Mixing PMem operating modes (App-direct, Memory mode) is not supported.

Population rules

- Maximum of one PMem per channel.
- If only one DIMM is populated on a channel, it should always go to the first slot in that channel (white slot).
- If a PMem and a DDR4 DIMM are populated on the same channel, always install PMem on the second slot (black slot).
- PMem cannot be mixed with other PMem capacities.
- Mixing different capacities of RDIMMs and LRDIMMs are not allowed when PMem is installed.
- PMems of different capacities are not allowed.
- VMware ESXi boot takes longer time when higher capacity of Intel PMem are configured in App Direct mode. This is
 expected as background ARS is going on the interleave sets and needs to be completed prior the pMem data store is
 mounted on ESXi.
- App Direct can be populated symmetrically or asymmetrically across sockets.
- In Memory mode symmetry across sockets is required.
- Populate PMem in DIMM slot 1, unless PMem is the only DIMM in that channel and then populate in DIMM slot 0.

If the PMem goal DIMMs prior to cr	requirement is changed eating new goal. This is	d between App Di s a requirement fo	rect and Memory or VMware ESXi.	Mode or vice-ver	sa, it is required to	sanitize tl

BIOS

Topics:

- BIOS configuration setting for Intel PMem
- App-direct mode configuration
- Memory mode configuration

BIOS configuration setting for Intel PMem

DIMM discovery

All installed PMems that the BIOS has discovered during system inventory is displayed in the BIOS Intel Persistent Memory tab:

Memory Settings > Persistent Memory > Intel Persistent Memory > Persistent Memory DIMM Configuration.

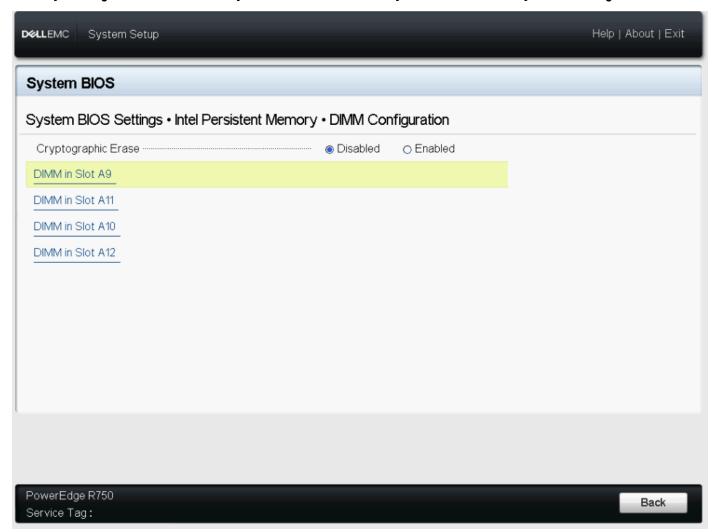


Figure 2. Persistent Memory screen

i NOTE: PMems are shown as DIMMs.

There is one entry for each PMem that is installed and the current health and status information for each PMem is displayed as:

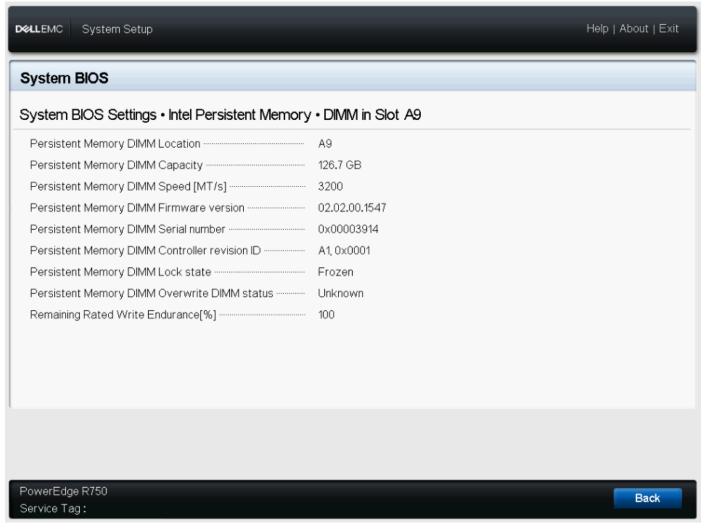


Figure 3. Memory information

- NOTE: Data is always assumed to be in units of MiB/GiB/TiB even if labeled MB/GB/TB. User capacity overhead is up to 2% of capacity (GiB). Another overhead may be required for Regions, Namespace, and Filesystems.
- NOTE: The default value of the Persistent Memory DIMM Overwrite DIMM status for PMem 100 Series is **not started** while the Persistent Memory DIMM Overwrite DIMM status for PMem 200 Series is **Unknown** after long operation.

Unknown - The overwrite PMem module operation status cannot be determined. This may occur if the status gets overwritten due to a different long operation running on this PMem module. This **unknown** status is expected behavior.

App-direct mode configuration

Create goal

Goal is created in BIOS.

To create a goal in BIOS, go to: Memory Settings > Persistent Memory > Intel Persistent Memory > Region Configuration > Create Goal Config.

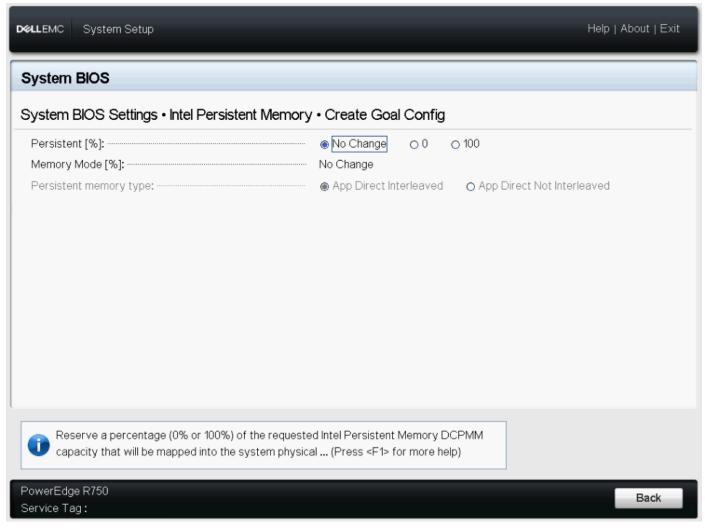


Figure 4. Goal configuration

Persistent [%]:

- No Change Does not apply any changes to the current goal.
- 100 Creates a goal of 100% Persistent memory across the selected PMems.
- 0 Creates a goal of 0% Persistent memory across the selected PMems. This operation configures all the PMem as Memory mode.

Persistent memory type:

- App-direct Interleaved Persistent mode interleave across the PMems in a socket. The PMems are displayed one PMem device per socket in the operating system.
- App-direct Not Interleaved Persistent mode is applied to PMem individually. Each PMem is displayed as an individual PMem device in the operating system.

After the goal is configured, and exited from BIOS, the goal will be created across the PMems using the user specified settings during the next boot.

Region information

Information about each region that is created during the **Create goal config** process can be accessed in the **Region Configuration** tab in BIOS after a system reset:

Memory Settings > Persistent Memory > Intel Persistent Memory > Region Configuration.

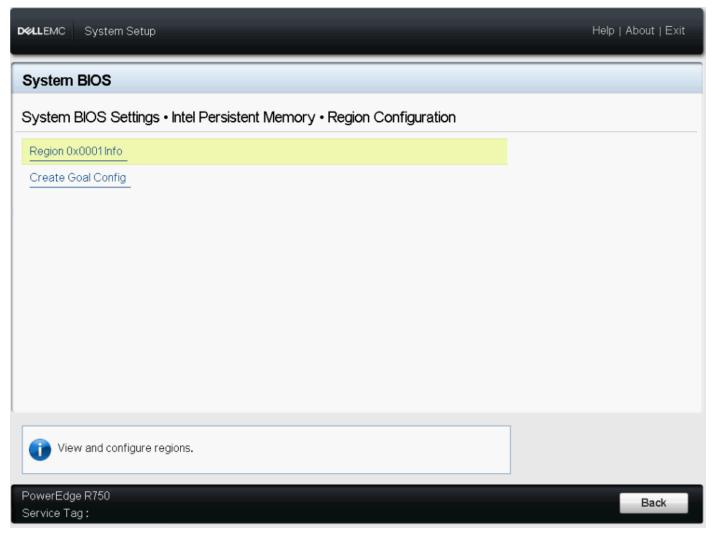


Figure 5. Region configuration

The number of regions that are displayed depends on the number of processors in the system and not on the PMems interleaved. If the PMems are configured as interleaved, one Persistent Memory Region is listed per socket in the system that has PMems installed. If the PMems are configured as non-interleaved, one **Persistent Memory Region** is listed per PMem installed in the system.

Region information can be accessed by clicking each **Persistent Memory Region** link in the BIOS. Following is an example:

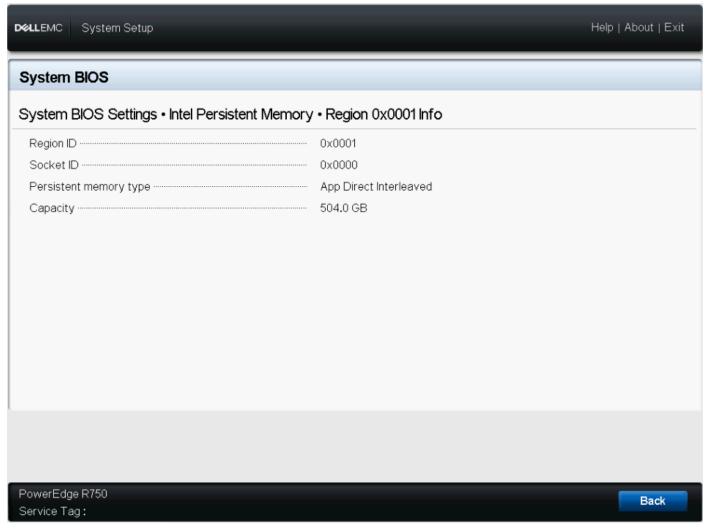


Figure 6. Region info

Memory mode configuration

Create goal

Goal is created in the BIOS.

To create a goal in BIOS, go to: Memory Settings > Persistent Memory > Intel Persistent Memory > Region Configuration > Create Goal Config.

The BIOS options determine how the goal is created and the PMems are configured:

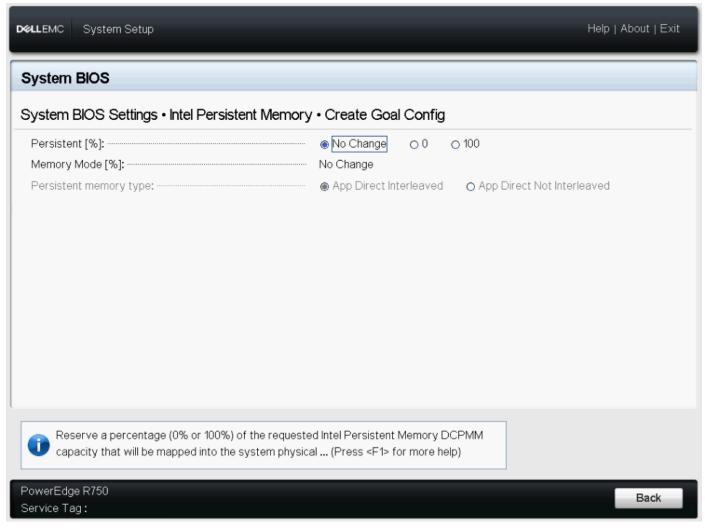


Figure 7. Create Goal Config

Persistent [%]:

- No Change Does not apply any changes to the current goal.
- 100 Creates a goal of 100% Persistent memory across the selected PMems.
- **0** Creates a goal of 0% Persistent memory across the selected PMems. This operation configures all the PMem as Memory mode.

PMem event reporting

When system detects PMem-related event either during runtime or POST, system will log the events in Server System Event Log(SEL) and Life-Cycle Log(LCL). If an event is detected during boot time, system halts during the POST and user needs to press F1 to continue the boot process.

NOTE: NVDIMM is frequently used in these messages. The term NVDIMM is generic to several different families of persistent memory including PMem and not meant to indicate NVDIMM-N modules.

Topics:

- · Event message ID encoding
- Events during boot time

Event message ID encoding

The event message IDs are encoded as MEM XYZZ.

- X Severity
 - 5 = Informational (LC log only)
 - 6 = Warning (LC log and SEL)
 - o 7 = Critical (LC log and SEL)
- Y Version
 - o 1 = Initial messages
- ZZ Recommended customer action
 - o 00 = No action
 - o 01 = Check config
 - o 02 = Reset DIMM
 - o 03 = Perform warm reset
 - o 04 = Perform cold reset
 - o 05 = Perform AC power cycle
 - o 06 = Contact customer support if problem persists
 - \circ 07 = Retry operation
 - o 08 = Sanitize or Factory Reset
 - o 09 = Secure Erase
 - o 10 = Update Firmware
 - o 14 = Contact customer support
 - o 99 = Generic or Unknown event

The Message ID and event message information is provided in the table.

Table 11. Message ID and Event Message

Message ID	Event Message	Severity
MEM5100	A diagnostic event occurred in the memory device at <location>. The server has automatically taken corrective action and the device is operating normally. (Debug Code: <code>).</code></location>	Info
MEM5101	A diagnostic event occurred in the memory device at <location>. Check the device and system configuration. (Debug Code: <code>).</code></location>	Info
MEM5102	A diagnostic event occurred in the memory device at <location>. Reseat the device. (Debug Code: <code>).</code></location>	Info

Table 11. Message ID and Event Message (continued)

Message ID	Event Message	Severity	
MEM5103	A diagnostic event occurred in the memory device at <location>. Perform a warm reboot. (Debug Code: <code>).</code></location>	Info	
MEM5104	A diagnostic event occurred in the memory device at <location>. Perform a cold reboot. (Debug Code: <code>).</code></location>	Info	
MEM5107	A diagnostic event occurred in the memory device at <location>. Retry operation. (Debug Code: <code>).</code></location>	Info	
MEM5109	A diagnostic event occurred in the memory device at <location>. The device is cryptographically locked. (Debug Code: <code>).</code></location>	Info	
MEM5199	An unknown diagnostic event occurred in the memory device at <location>. (Debug Code: <code>).</code></location>	Info	
MEM6101	A diagnostic warning event occurred in the memory device at <location>. Check the device and system configuration. (Debug Code: <code>).</code></location>	Warning	
MEM6102	A diagnostic warning event occurred in the memory device at <location>. Reseat the device. (Debug Code: <code>).</code></location>	Warning	
MEM6104	A diagnostic warning event occurred in the memory device at <location>. Perform a cold reboot. (Debug Code: <code>).</code></location>	Warning	
MEM6106	A diagnostic warning event occurred in the memory device at <location>. Contact service provider if issue persists. (Debug Code: <code>).</code></location>	Warning	
MEM6107	A diagnostic warning event occurred in the memory device at <location>. Retry operation. (Debug Code: <code>).</code></location>	Warning	
MEM6110	A diagnostic warning event occurred in the memory device at <location>. Update device firmware. (Debug Code: <code>).</code></location>	Warning	
MEM6114	A diagnostic warning event occurred in the memory device at <location>. Contact your service provider for assistance. (Debug Code: <code>).</code></location>	Warning	
MEM6199	An unknown diagnostic warning event occurred in the memory device at <location>. (Debug Code: <code>).</code></location>	Warning	
MEM7101	A critical diagnostic event occurred in the memory device at <location>. Check the device and system configuration. (Debug Code: <code>).</code></location>	Critical	
MEM7102	A critical diagnostic event occurred in the memory device at <location>. Reseat the device. (Debug Code: <code>).</code></location>	Critical	
MEM7104	A critical diagnostic event occurred in the memory device at <location>. Perform a cold reboot. (Debug Code: <code>).</code></location>	Critical	
MEM7107	A critical diagnostic event occurred in the memory device at <location>. Retry operation. (Debug Code: <code>).</code></location>	Critical	
MEM7108	A critical diagnostic event occurred in the memory device at <location>. Sanitize the device. (Debug Code: <code>).</code></location>	Critical	
MEM7110	A critical diagnostic event occurred in the memory device at <location>. Update device firmware. (Debug Code: <code>).</code></location>		
MEM7114	A critical diagnostic event occurred in the memory device at <location>. Contact your service provider for assistance. (Debug Code: <code>).</code></location>	Critical	
MEM7199	An unknown critical diagnostic event occurred in the memory device at <location>. (Debug Code: <code>).</code></location>	Critical	

Events during boot time

- **UEFI0337**: Unable to update the firmware of NVDIMM located in the memory slot <slot number>. **Recommended Action**: Disconnect the input power to the system, wait for 30 seconds, and reconnect the power, Power on the server and retry the operation. If the issue persists, replace the NVDIMM. For more information, see the product's owner's manual on the support site.
- UEFI0338: The firmware of NVDIMM located in the memory slot <slot number> is successfully updated. Recommended
 Action: N/A
- **UEFI0345**: The erase operation on the nonvolatile DIMM with serial number <serial number> in slot <slot number> is successfully completed.

Recommended Action: N/A

• **UEFI0347**: Unable to initialize the memory because one or more errors have occurred during the NVDIMM initialization in the slot <slot Number>.

Recommended Action: Manually remove and reinstall the NVDIMM. If the issue persists, contact your service provider. For more information about removing and reinstalling an NVDIMM, see the product Installation and Service Manual available on the support site.

• UEFI0348: The Remaining Rated Endurance value of NVDIMM installed in slot <slot Number> is less than or equal to 1%.

Recommended Action: Power off the server and replace the NVDIMM immediately. For more information about the Remaining Rated Endurance, see the product Installation and Service Manual available on the support site.

• **UEFI0349**: Unable to initialize the NVDIMMs because different types of NVDIMM SKUs or controller revisions are installed.

Recommended Action: Power off the server and replace the NVDIMMs to ensure all NVDIMM SKUs or controller revisions are same. For more information about the NVDIMMs SKUs or controller revision, see the product Installation and Service Manual available on the support site.

• **UEFI0350**: The NVDIMM in the memory slot <slot Number> is replaced or removed from a previously configured Persistent Memory (PM) region.

Recommended Action: Create a new Persistent Memory (PM) region. For more information about creating a PM region, see the platform Installation and Service Manual available on the support site.

• **UEFI0351**: The NVDIMMs on socket <socket Number> are installed in an unsupported (configuration) manner. Major Error code <major MRC error code> Minor Error code <minor MRC error code>. Data in the Persistent Memory (PM) region may not be accessible.

Recommended Action: For more information about creating a PM region, see the platform Installation and Service Manual available on the support site.

• **UEFI0352**: Unable to use the Persistent Memory (PM) region configuration of the NVDIMM in the memory slot <slot Number>.

Recommended Action: Verify the NVDIMM population configuration and retry the operation. For more information about NVDIMMs, see the platform Installation and Service Manual available on the support site.

• **UEFI0353**: The Persistent Memory (PM) configuration information of the NVDIMM in the memory slot <slot Number> is corrupted because the checksum or the header type is not valid.

Recommended Action: Remove and reinstall the NVDIMMs or create a new Persistent Memory (PM) configuration. If the issue persists, contact your service provider. For more information about creating a PM region, see the platform Installation and Service Manual available on the support site.

• **UEFI0354**: The ratio of DDR4 memory to NVDIMM on the socket <socket Name> is not optimal to deliver the best performance.

Recommended Action: Reconfigure the ratio of DDR4 memory to NVDIMM to a value between 1:4 and 1:16. For more information about reconfiguring the DDR4 memory, see the platform Installation and Service Manual available on the support site

• **UEFI0355**: All the NVDIMMs are disabled because the value of memory capacity of all the installed NVDIMMs has exceeded the maximum value supported by the processor.

Recommended Action: Reconfigure the processor to support the value of memory capacity of all the installed NVDIMMs. For more information about reconfiguring the processor, see the platform Installation and Service Manual available on the support site.

• **UEFI0356**: The data in the Persistent Memory DIMM located in memory slot <slot label> is not accessible because the DIMM is locked and the passphrase is incorrect.

Recommended Action: Update the Persistent Memory Passphrase to the correct passphrase or perform a Secure Erase operation on the Dual inline memory module (DIMM). Secure erase, erases all persistent data.

• **UEFI0357**: The Cryptographic Erase operation on the Intel Persistent Memory DIMM with serial number <serial number> in slot <slot number> is successfully completed.

Recommended Action: N/A

• **UEFI0358**: Unable to complete the Cryptographic Erase operation on the Intel Persistent Memory DIMM with serial number <serial number> in slot <slot number>.

Recommended Action: Retry the operation. If the issue persists, contact your service provider.

• **UEFI0359**: The Overwrite DIMM operation on the Intel Persistent Memory DIMM with serial number <serial number> in slot <slot number> is successfully completed.

Recommended Action: N/A

NOTE: This is part of the PMem Sanitize function.

 UEFI0360: Unable to complete the Overwrite DIMM operation on the Intel Persistent Memory DIMM with serial number <serial number> in slot <slot number>.

Recommended Action: Retry the operation. If the issue persists, contact your service provider.

NOTE: This is part of the PMem Sanitize function.

UEFI0361: The Factory Default operation for the Intel Persistent Memory DIMMs in the system has completed successfully.

Recommended Action: N/A

- **UEFI0362**: Unable to complete the Factory Default operation on the Intel Persistent Memory DIMMs. **Recommended Action**: Retry the operation. If the issue persists, contact your service provider.
- UEFI0367: The Create Goal operation on the Intel Persistent Memory DIMMs is successfully completed.

Recommended Action: N/A

- **UEFI0368**: Unable to complete the Create Goal operation on the Intel Persistent Memory DIMMs. **Recommended Action**: Retry the operation. If the issue persists, contact your service provider.
- **UEFI0369**: The Complex Programmable Logic Device (CPLD) is successfully armed for the Asynchronous DRAM Refresh (ADR) signal.

Recommended Action: N/A

• **UEFI0370**: The Complex Programmable Logic Device (CPLD) cannot arm for the Asynchronous DRAM Refresh (ADR) signal. It might be due to Intel Persistent Memory DIMMs failed to initialize.

Recommended Action: Reboot the system. If the issue persists, contact your service provider.

• UEFI0372: The Remaining Rated Endurance value of the NVDIMM installed in the slot <slot number> is equal to 0%.

Recommended Action: Turn off the server and replace the NVDIMM immediately. For more information about the Remaining Rated Endurance, see the product Installation and Service Manual available on the support site.

- **UEFI0373**: The NVDIMM installed in slot <slot number> requires a maintenance. **Recommended Action**: Consider replacing the DIMM during the next maintenance cycle. For more information about the NVDIMM Health Status, see the product Installation and Service Manual available on the support site.
- **UEFI0374**: The NVDIMM installed in slot <slot Number> is in critical condition. **Recommended Action**: Power off the server and replace the NVDIMM immediately. For more information about the NVDIMM Health Status, see the product Installation and Service Manual available on the support site.
- **UEFI0375**: Unable to apply the Persistent Memory (PM) region configuration of the NVDIMM in the memory slot <slot Number>.

Recommended Action: Verify the NVDIMM population configuration and retry the operation. For more information about NVDIMMs, see the platform Installation and Service Manual available on the support site.

 UEFI0376: All the NVDIMMs are disabled because the installed processor does not support the Intel Persistent Memory DIMMs.

Recommended Action: Reconfigure the processor to support the Intel Persistent Memory DIMMs. For more information about reconfiguring the processor, see the Installation and Service Manual of the system available on the support site.

• **UEFI0377**: The Non-Volatile Dual In-Line memory module (NVDIMM) in the memory slot <slotNum> has encountered a transient fatal failure during the previous boot.

Recommended Action: Remove and reinstall the Non-Volatile Dual In-line memory module (NVDIMM) device. If the issue persists, contact your service provider. For more information about installation and removal, see the Installation and Service Manual of the server available on the support site.

• **UEFI0378**: The Non-Volatile Dual In-Line memory module (NVDIMM) in the memory slot <slotNum> has encountered a thermal shutdown event during the previous boot.

Recommended Action: Do one of the following: 1) Power off the server. 2) Disconnect the input power, wait for 30 seconds, and then reconnect to the power source. 3) Power on the server. 4) If the issue persists, contact your service provider.

• **PWR2281**: Unable to perform the memory arming operation because the PSU configuration of the server is insufficient to guarantee data flush time in the event of power loss.

Recommended Action: Do the following and retry the operation:

- o Turn off the server.
- Ensure the PSUs are installed as recommended in the user guide.
- o Turn on the server.

For more information on supported PSUs, see the Installation and Service Manual of the system available on the support site.

iDRAC Intel PMem management

Topics:

iDRAC GUI

iDRAC GUI

PMem firmware version

The PMem firmware version is displayed under Configuration > System BIOS Settings > Memory Settings > Persistent Memory > Intel Persistent Memory >

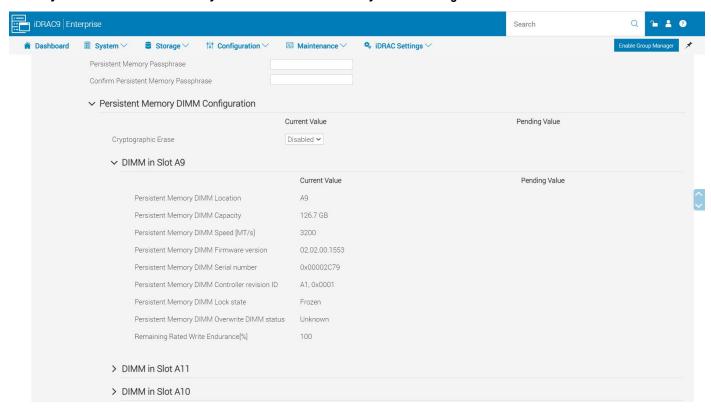


Figure 8. PMem firmware version

PMem hardware status

Select the Memory link on the Dashboard to get more information about memory health.

PMem hardware status is displayed under **System** > **Inventory** > **Hardware inventory**.

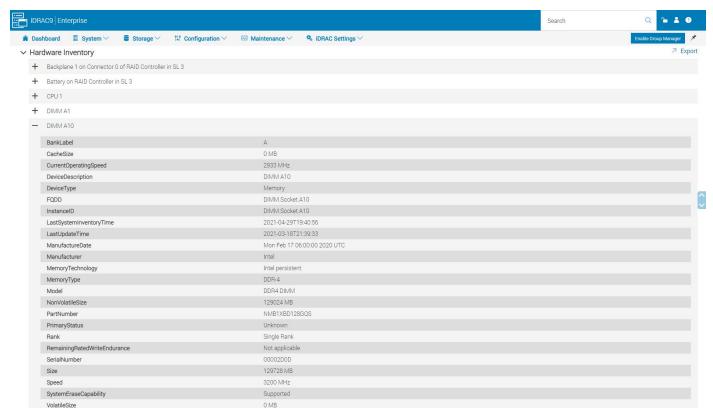


Figure 9. PMem hardware status

PMem goal configuration using iDRAC GUI

- 1. Log on to iDRAC GUI interface.
- $\textbf{2.} \quad \text{Navigate to Configuration} > \textbf{BIOS settings} > \textbf{Memory settings} > \textbf{Persistent Memory Settings tab}.$



Figure 10. PMem goal configuration using iDRAC GUI

3. Navigate to Intel Persistent Memory > Region Configuration > Create Goal Config.



Figure 11. PMem goal configuration

- **4.** Change Persistent percentage as 100% to configure Intel PMem into 100% App-direct Mode and 0% to configure the DIMMs into 100% memory mode.
 - NOTE: The Persistent memory type field is for configuring a new goal. It is not for reading the status of the current goal. Use instructions under PMem Hardware Status to confirm current configuration.
- 5. Click Apply and reset the system.
- 6. Goals will be applied into operating modes (either App-direct or Memory) in the next power cycle.

PMem remaining rated write endurance

PMem remaining lifetime is reflected as **Remaining Rated Write Endurance** in the IDRAC GUI. It is displayed under **System > Overview > Memory**.

Key limitations and feature information:

- The feature does not work if the system is halted in BIOS such as the BIOS setup. If the system stays in this state for long periods, the endurance for all PMem reports as 0% as status cannot be retrieved. To resolve, the user needs to boot into the operating system and wait up to 24 hours for the next automatic poll of status.
- The values do not change frequently and are polled once per day. If the user performs a System Erase or Repurpose and Retire, it destroys the stored values for this feature. PMem will be displayed as 0% after such an operation until the next automatic poll within 24 hours.



Figure 12. Individual memory details

PMem security

Topics:

- Memory mode
- App-direct
- Cryptographic erase and PMem sanitize

Memory mode

In Memory mode PMems operate as volatile system memory. User passphrase is not supported and this BIOS setting will be greyed out.

App-direct

Users have the option to enable Passphrase protection of PMem regions. The intent of the passphrase is to protect against unauthorized access to data stored on the PMem region. If the PMems are moved from one server to another server, the user must re-enter the security passphrase in BIOS setup before the data can be accessed.

If the customer chooses to enable passphrase protection or not, BIOS locks the PMem before booting to the operating system or UEFI Shell. This means that all security changes are controlled by the Dell BIOS and operating system level security changes including Passphrase management and PMem erasing functions will not be supported. All these functions must be driven through the BIOS setup.

NOTE: As mentioned in section DIMM Configuration Changes, the only migration scenario that is supported is a slot for slot replacement between motherboards. Adding or removing individual PMem for any reason will likely result in data loss and trigger the need for goal and security reconfiguration.

The passphrase to lock or encrypt the data at rest on the PMem in App-direct is configurable in the BIOS setup. If the field is not empty, every boot the supplied passphrase is used to attempt to unlock all PMem in the system.

The following use cases are related to unsupported migration scenarios:

- When changing passphrase in the BIOS setup, the existing passphrase only needs to be entered once per session. Entering and existing the field multiple times will not reprompt for the passphrase again (until the next boot session).
- Passphrase can be cleared by entering empty string in BIOS setup passphrase field.
- (i) NOTE: To clear the passphrase, keep the passphrase field blank and hit Enter.

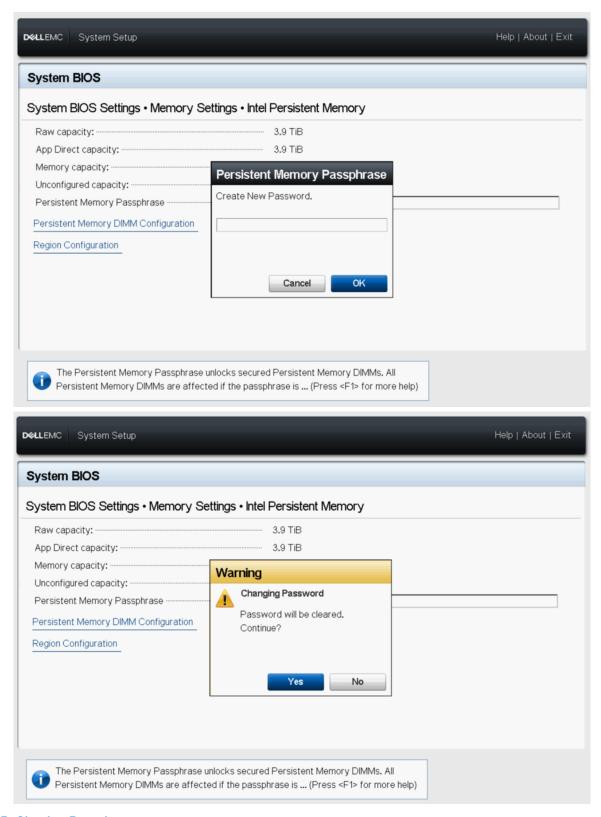


Figure 13. Clearing Passphrase

Cryptographic erase and PMem sanitize

There are two ways to erase the persistent region content (App-direct) of the PMem:

Crypto Erase

Sanitize

Both erase methods can be executed using BIOS setup options. User can choose to perform an erase on all or a subset of installed PMems.

Crypto erase

The Crypto Erase function erases the App-direct Region Key (PM-RK) forcing the system to reboot.

Cryptographic erase option can be accessed by going to: System BIOS Settings > Persistent Memory > Intel Persistent Memory > DIMM Configuration



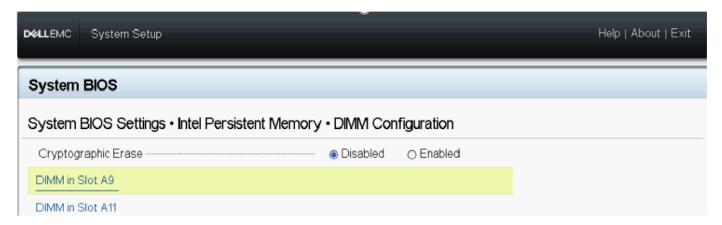


Figure 14. Crypto erase

NOTE: It is not recommended to erase part of PMems installed on the system when App-direct interleaved region is configured. This operation renders all the data on the interleave set invalid.

Sanitize

PMem Sanitization is a long operation which runs on all the selected persistent memories in parallel.

This process does a cryptographic erase first and writes zeros to all accessible persistent media regions on the PMem. Also, it destroys any existing memory goal contents.

The region of the PMem where the goal is configured will be empty and on the next boot the memory will default to 100% Memory Mode. If the system does not have the proper ratio of RDIMM or LRDIMM memory to PMem, it is expected on the following boot for an error to occur indicating that the ratio is not optimized.

Sanitize option can be accessed by going to: System BIOS Settings > Persistent Memory

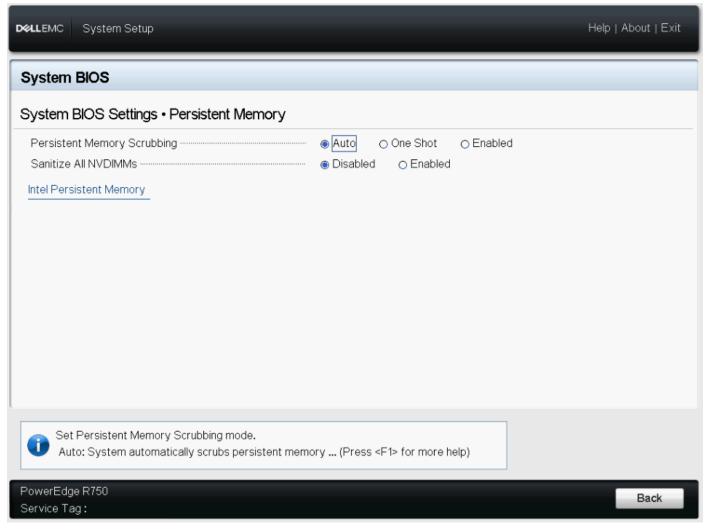


Figure 15. Sanitize

Sanitize can take up to 15 minutes with fully loaded 128 GB DIMM configuration, 30 minutes with 256 GB and 1 hour with 512 GB.

(i) NOTE: When the Sanitize operation is running, a prompt appears in BIOS indicating an Overwrite. Overwrite is the name for the second firmware command that is conducted. The first command which happens quickly and will not be displayed on-screen is the Crypto Erase (firmware command name is "Secure Erase").

DIMM configuration changes

The following PMem migration scenarios are supported:

- Replacement of System Board due to failure
 - All DIMMs must be re-populated in the exact same slots. PMems and data content will be available for customer application access after the board has been restored to the same configuration as the original board. System Restore will automatically restore the BIOS configuration on the replacement board, including the PMem Passphrase, if it is set.
- Replacing bad DIMM
 - In the case of a failed PMem, any data associated with the PMem would be lost. The region and interleave set on the failed PMem must be recreated once it is replaced. The user must use BIOS Setup to create a new goal for the affected PMem.
- NOTE: Any remaining persistent memory data on Intel PMem must be backed up prior creating a goal. The goal creation process deletes all namespaces, regions, and data stored on the PMems on the selected CPUs. If a Security Passphrase is enabled, the new persistent memory region is protected with the system PMem passphrase.
- NOTE: Adding or removing PMems to an existing PMem configuration is **not supported**, and not validated. It is recommended that customers back up all PMem data to another storage device before making any PMem configuration changes. Once the server is reconfigured to the new PMem configuration, the customer can create a goal configuration and restore data back to PMems.

Windows

Dell EMC supports Intel Optane PMem with Microsoft Windows 2019 in Memory mode and App-direct mode.

i NOTE: Keep Windows updated with the monthly cumulative updates.

Topics:

- PMem in App-direct mode
- PMem disk with interleave sets
- PMem in memory Mode
- · Windows troubleshooting and event monitoring
- Windows Errata

PMem in App-direct mode

In App-direct mode, Windows creates two types of device objects for PMems :

- Physical INVDIMM device
- Logical Persistent Memory disk

Logical persistent memory disks get created after creating namespaces on PMem Physical disks.

When the system first boots into the Operating System, PMem devices get enumerated as physical INVDIMM devices under Memory Devices in Device Manager.

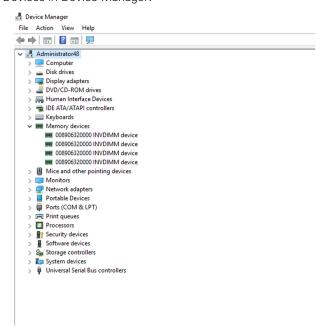


Figure 16. Memory Devices in Device Manager

After PMem disks are configured over PowerShell, logical persistent memory disks show under Device Manager.

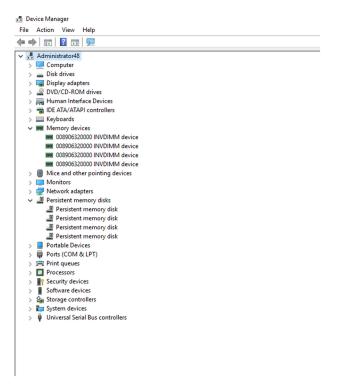


Figure 17. Memory Devices in Device Manager

PMem Disk management

Windows currently supports only one namespace per interleave set (this is independent of the number of physical devices in the interleave set). The option to interleave PMems can be selected during goal creation as described in App Direct and Memory mode configurations.

PMem disks have to be created with the help of "New-Pmemdisk" command by providing relevant region IDs. Once PMem disks are configured, PMem volumes can be used as normal disks.

Windows supports the following PowerShell cmdlets to manage persistent memory:

- Get-PmemDisk
 - o Returns one or more logical persistent memory disks.
 - o The returned object has information about size, atomicity type, health status, and underlying physical devices.
- Get-PmemPhysicalDevice
 - o Returns one or more physical persistent memory devices (NVDIMMs).
 - \circ The returned object has information about size, RFIC, device location, and health/operational status.
- New-PmemDisk
 - o Creates a new disk out of a given unused region.
 - Writes out the labels to create the namespace then rebuilds the SCM stacks to expose the new logical device.
 - o Optional parameters:
 - FriendlyName gives the persistent memory disk a friendly name. Default is "PmemDisk<N>".
 - AtomicityType lets you set BTT. Default is "none."
- Remove-PmemDisk
 - $\circ \quad \text{Removes the given persistent memory disk. It accepts the output of $\tt Get-PmemDisk.}$
 - o Deletes the namespace's labels and then rebuilds the SCM stacks to remove the logical device.
 - o Requires user confirmation, which can be overridden with Force.
- Get-PmemUnusedRegion
 - o Returns aggregate PMem regions available for provisioning a logical device.
 - Returned object has a unique region ID, total size, and list of physical devices that contribute to the unused region.
- Initialize-PmemPhysicalDevice
 - Writes zeroes to the label storage area, writes new label index blocks, and then rebuilds the SCM stacks to reflect the changes.

- o Requires user confirmation, which can be overridden with Force.
- o This cmdlet is intended as a "big hammer" recovery mechanism. It is not recommended for normal use.

List PMem physical disks and check their health status

The following image displays command usage to list all PMem physical devices and get their health. Physical location gives the location of the DIMM on the motherboard.

Figure 18. List PMem Physical Disks and their health status

NOTE: If Health Status is not Healthy and Operational Status is not OK, the issue needs to be rectified before creating namespaces. Customers can run into this scenario if PMems were previously used with another operating system and were booted with Windows without Sanitizing. If such a scenario occurs, right-click and uninstall all the Memory devices and Persistent Memory disks from Device Manager and then scan for HW changes under Action in Device Manager. This resolves any issues with the driver stack.

Create PMem Disks

Figure 19. Create PMem Disks

Remove PMem disks

Figure 20. Remove PMem disks

Windows 2019 does not support redundant volume creation on PMem disks by using Windows VDS (Virtual disk service).

In order to create redundant volumes, use the storage spaces method.

For information about storage spaces method refer to: https://docs.microsoft.com/en-us/windows-server/storage/storage-spaces/deploy-standalone-storage-spaces.

PMem disk with interleave sets

Interleaved sets can often be created to make multiple persistent memory devices be displayed as a single logical disk to windows server. For PMem Disk with the interleave set, "App-direct Interleave" should be selected during goal configuration.

PMem disk creation with interleave sets

When Interleave set is enabled, BIOS assigns single Region Id for PMems connected to the same processor.

Figure 21. PMem Disk creation with interleave sets

In above example two Interleave sets have been created, one Interleave set using slots A7, A8 and another Interleave set using B7, B8.

We can create PMem Disk also using Region id.

Figure 22. PMem Disk creation using Region id

PMem in memory Mode

When Intel Optane PMem is configured in memory mode, operating system sees it as system memory.

Persistent memory size is shown as zero and volatile memory size accounts for the entire size of the PMem.

Figure 23. PMem in memory Mode

Windows troubleshooting and event monitoring

If any of the PMem physical devices or logical devices are not functioning properly, it is suggested to check Windows event logs.

To see the logs, open Event Viewer and navigate to : Applications and Services Logs > Microsoft > Windows

The names of all persistent memory drivers logs start with "PersistentMemory."

All runtime errors are logged to the "Operational" log. This log captures the complete operation of PMem physical device (NVDIMM) and PMem logical device (PMemDisk).

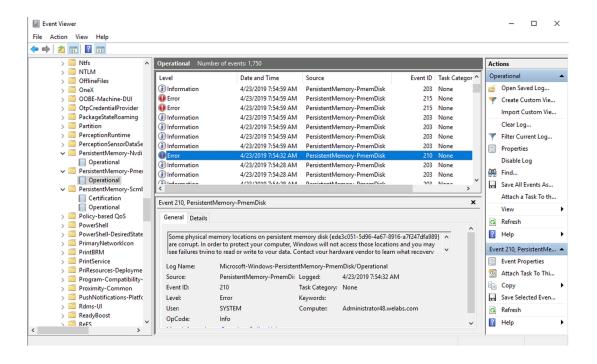


Figure 24. Windows troubleshooting and event monitoring

Windows Errata

The following errata affects Windows OSes and are expected to be fixed in a future OS patch.

• When a namespace is created in Windows Server 2019 (WS2019), the logged message appears as "The driver for persistent memory disk encounters internal error". The error is expected and it may occur during testing of Storage Class Memory (SCM) device. This means the namespace is created, and is considered as WAD for WS2019.

Workaround: None.

Linux

Topics:

- Identify and configure persistent memory device
- Management utility
- Linux errata

Identify and configure persistent memory device

Listing PMem devices

To list all physical devices in the system, run command

ndctl list -DHi

Create namespace

The configuration of namespaces will decide how much memory capacity user wants to expose to the OS.

To configure namespace, run the following command:

```
ndctl create-namespace
```

Following command needs to be executed for each region created when the goals were created for the system:

```
ndctl create-namespace -r regionX
```

After creating name spaces, all name spaces are shown using the following command:

```
ndctl list -N
```

To correlate a namespace to a PMem device, use the following command:

lsblk

NOTE: Ensure you delete all the namespaces before repurposing the PMems. Repurposing includes PMem migration, removal and reconfiguration between memory and App-direct mode

Mount file system on namespace device

After you see the PMem devices in /device folder, mount the filesystems by using the following commands:

```
mkfs.xfs -f -m reflink=0 /dev/pmemX
```

(X is a natural number ranging from 0 to number of Logical PMem Devices)

```
mkdir /mnt/pmemX
mount -o dax /dev/pmemX /mnt/pmemX
```

To write data into the device, run the following command:

```
cd /mnt/pmemX
echo "Hello World" >>test.txt
```

Reset the system and the data should be persistent over power cycle.

Delete namespaces

Namespace can be deleted using NDCTL command:

```
ndctl destroy-namespace <namespace>
```

<namespace> is namespaceX.Y device name which can be retrieved using the following command:

```
ndctl list -N
```

To delete all the namespaces on the system, you may have to send the command multiple times

i NOTE: Reconfiguration of the goal or region from BIOS setup will automatically delete all the namespace on the system.

Management utility

Linux distributions use native tool ndctl to manage nonvolatile devices.

For comprehensive list of commands and syntax, see open-source resource pmem.io.

Check PMem health status

Following NDCTL command shows the health status of each PMems installed on the system:

ndctl list -DHi

Health Information includes:

Table 12. Health information

Health fields	Comment
Overall Health State	Critical, Non-critical, ok
DIMM Temperature	In degree C
Spare Percentage/Lifetime percentage	In %
User configured alarm temperature	Smart controller temperature threshold for alarm
User configured alarm spares	Smart spares threshold for alarm
Shutdown state	Dirty or Clean. Determines if data has been successfully cleaned to persistent domain during previous power cycle.

Linux errata

1. If an uncorrectable error occurs in the early metadata region of the PMem, the system can get into an infinite loop of being unresponsive when booting into Linux.

Workaround: Boot with "modprobe.blacklist=nd_pmem" on the kernel command line to stop the consumption of the error and enable boot progress. Afterwards, wait sufficient time for the Address Range Scrub feature to scrub the memory and identify the bad address location so it will be mapped out on future boots.

2. Linux boot fails to Emergency Mode when PMem is configured to automount during boot in fstab.

Workaround: Add "x-systemd.device-timeout=0" to the mount options for the PMem partition in fstab.

Example: /dev/pmem5 /mnt/somedir ext4 defaults, x-systemd.device-timeout=0,dax 0 2

3. Dell BIOS does not support boot from PMem. Some Linux operating system (Red Hat Enterprise Linux 7.6, Red Hat Enterprise Linux 8.0) distros are offering early support of this feature, but Dell BIOS does not support this feature.

Workaround: None.

VMware ESXi

Dell EMC supports Intel Optane PMem from VMWare ESXi 7.0 Update 2 and above. The support is available for Intel PMem Memory Mode and App Direct modes.

The persistent memory inventory details are available as part of Host client. See **Managing Persistent Memory** in the **vSphere Single Host Management - VMware Host Client** which detail the persistent memory data that are presented to the user.

Following sections talk about how ESXi displays the persistent memory details in different operating modes.

Topics:

- PMem in App Direct mode
- PMem in Memory Mode
- PMem health status
- ESXi troubleshooting and maintenance

PMem in App Direct mode

Log in to the Host client using ESXi credentials. Go to **Storage > Persistent Memory** to view the persistent memory **Modules**, the **Interleave sets** created, and the **Namespaces** that are created on ESXi.



Figure 25. Persistent Memory modules populated in the system

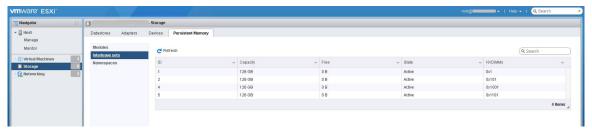


Figure 26. Number of Interleave sets created on the system

Select **Interleaved** when creating a goal. The number of interleaved sets that are exposed will be equal to the number of CPU sockets in the system. Unlike Linux, ESXi automatically creates name spaces on the interleave sets exposed. Dell EMC does not support **Non-interleaved** App Direct goals with ESXi.

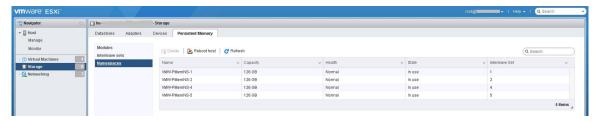


Figure 27. Namespaces created on ESXi out of the interleave sets exposed

After creating the Namespaces, ESXi automatically creates a PMem datastore and mounts it as a datastore for the users to consume it.



Figure 28. PMem Datastore

PMem in Memory Mode

When Intel PMem is configured in **Memory Mode**, ESXi sees it as system memory.

NOTE: The inventory details in the vSphere HTML client will not be available if Intel PMem is set to Memory Mode. There is no datastore created in memory mode as ESXi creates in Intel PMem App Direct mode.

PMem health status

ESXi provides multiple health status for PMem such as maintenance needed, all data loss, and normal.

ESXi reports the health of the specific PMems as maintenance needed, when there is a health error.

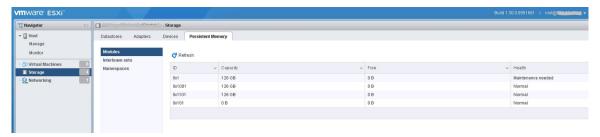


Figure 29. PMem health warning

ESXi reports the health of the specific PMems as all data loss, when there is a fatal error.

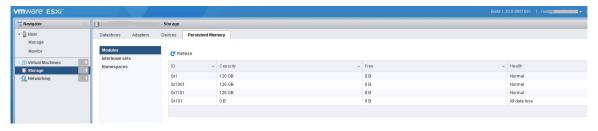


Figure 30. PMem multibit error

ESXi troubleshooting and maintenance

- NOTE: Go through/var/log/vmkernel.log to see if there are any errors or warnings reported related to persistent memory. The log entries related to persistent memory would be appended with NVD and or IntelNVDimm.
- Configuring Intel PMem in memory mode shows a reduction of system memory in system BIOS.
 - For example, a system configuration with 4 * 128 GB Intel PMems, configured in memory mode provides a system memory of 506.8 GB instead of 512 GB.
 - o It is for reserving space for metadata.
- PMem datastore is not mounted automatically in ESXi.
 - o See /var/log/vmkernel.log and check for shut down counter related errors to DIMM handles.
 - o When there is a critical error to any of the Intel PMems, datastore is not mounted by ESXi.
- ESXi boot takes longer when Intel PMems are configured as Non-interleaved App Direct. Dell EMC does not support Non-interleaved App Direct goals with ESXi.
- If PMems contain a critical error, the ESXi Host Client (HTML client) GUI does not respond when the user moves to Persistent Memory section under Storage. The host server is unresponsive when it reads the PMem region address range with the critical error. For more information see VMware KB 70661: https://kb.vmware.com/s/article/70661.
- In ESXi, Windows as a guest operating system does not display Storage Class Memory device in the device manager, for the PMem devices that are attached to the virtual machine (VM).
 - Virtual BIOS for the VM hides the device from device manager. User can use the PowerShell cmdlets to view the PMem devices that are attached to the windows guest operating systems running on ESXi.
- esxtop utility in ESXi exposes some dummy performance counters when PMems are configured in the system. Counter
 names might be displayed as 'Cache hit', 'Cache miss', 'nBuffers', 'Flush interval'. These are dummy performance counters
 and no functionalities that are associated with it for now.
- ESXi displays more number of NUMA nodes on systems that are configured with PMems in App-direct mode. This is an expected behavior as the NUMA nodes are created for volatile and nonvolatile address ranges. The nonvolatile address ranges are displayed as 0 MB.
- ESXi logs a warning in the Vmkernel log Unable to register PMem file system for APD notifications. This log entry can be safely ignored as the PSA (Pluggable Storage Architecture) APD(All Paths Down) plug-in is not supported for PMem type volumes. For more information see VMware KB 2145444: https://kb.vmware.com/s/article/2145444.
- When persistent memory goals are changed in sequence from App Direct mode to Memory mode and back to App Direct mode, PMem datastore does not mount automatically in ESXi. This occurs when ESXi is unable to format and mount the Namespaces when the App Direct mode is re-created. As a solution, perform sanitization operation before creating a new goal.

System diagnostics

System diagnostics in the Lifecycle Controller does not test Intel PMem in App Direct mode to avoid destroying customer data.

NOTE: System diagnostics are not recommended to investigate Intel PMem failures when in Memory mode due to extremely long test time.

Firmware update

Topics:

Dell DUP update

Dell DUP update

Download PMem DUP from www.dell.com/support/drivers.

- i NOTE: Make sure BIOS is up to date with the latest version before applying PMem DUP.
- 1. Boot to the operating system and execute DUP package.
 - a. On a Windows system, double click the .exe DUP file. Reboot the system and DUP will be automatically executed.
 - **b.** On Linux system:
 - i. Navigate to the folder that contains DUP bin file
 - ii. Give read write permissions to the DUP bin file (Chmod 777)
 - iii. Execute the DUP bin file (./)
- 2. If operating systems is not accessible, log in to iDRAC. Navigate to **Maintenance** > **System Update**, upload and install the .exe file downloaded from the Dell support site.

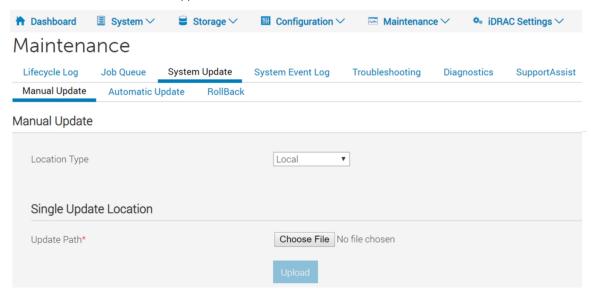


Figure 31. System update via iDRAC

- 3. The system will automatically reboot and flash the firmware using Lifecycle Controller. Multiple reboots are expected.
- 4. Firmware can also be updated using the Lifecycle Controller:
 - a. Launch Lifecycle controller (LC) during boot.
 - **b.** Launch Firmware Update from Lifecycle Controller.
 - c. Select FTP server or local drive that hosts the downloaded DUP and launch the update.
 - d. DUP update gets staged through LC.

For more information on updating firmware's using Lifecycle Controller see: Lifecycle Controller User's Guide available at https://www.dell.com/idracmanuals .