



Statement of Volatility – Dell EMC PowerEdge MX5000s

The Statement of Volatility provides you the information related to volatile and non-volatile components of different configurations of Dell EMC PowerEdge servers.

Volatile components lose their data when power cord is removed from the system, whereas, non-volatile components continue to retain their data when the power has been removed from the component.


The following table provides information of different configurations of the PowerEdge MX5000s.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write-protected?	How is memory cleared?
Main Board										
EXP SPI Flash	Non-Volatile	1	U_EXP_SPIROM	128 Mb	SPI Flash	No	EXP operation firmware storage	SPI interface via EXP	Hardware strapping	Not user clearable
EXP EEPROM	Non-Volatile	1	U3001	32 Mb	EEPROM	No	EXP configuration data	SPI interface via EXP	Hardware strapping	Not user clearable

EXP SEEPROM	Non-Volatile	1	U3002	2 Kb	EEPROM	No	EXP log storage	I2C interface via EXP	Hardware strapping	Not user clearable
FMP	Volatile	1	U_88F6810C	32 KB	Cache + registers	Yes	Processor cache + registers	Various	Various	Power off
FMP SPI Boot	Non-Volatile	1	U_UBOOT1	32 Mb	Serial Flash	No	FMP uboot code	SPI interface via FMP	Hardware strapping	Not user clearable
FMP eMMC	Non-Volatile	1	U_FMP_EMMC1	8 GB	eMMC NAND Flash	No	Operational iDRAC FW, Lifecycle Controller (LC) USC partition, LC service diags, LC OS drivers, USC firmware	NAND Flash interface via FMP	Embedded FW write protected	Not user clearable
FMP DDR	Volatile	3	U_FMP_DDR1, U_FMP_DDR2, U_FMP_ECC	8 Gb	DRAM	No	FMP local memory	FMP Firmware	NA	Power off

IOC Boot	Non-Volatile	1	U20	256 Kb	Serial EEPROM	No	IOC configuration data	I2C interface via IOC	Hardware strapping	Not user clearable
IOC SPI Flash	Non-Volatile	1	U_IOC_NORFLASH	256 Mb	Serial NOR Flash	No	IOC operation firmware storage	SPI interface via IOC	Hardware strapping	Not user clearable
IOC NVSRAM	Non-Volatile	1	U_IOC_NVSRAM1	1 Mb	SPI MRAM	No	Critical log storage	SPI interface via IOC	Hardware strapping	Not user clearable
FReD Flash	Non-Volatile	1	U_FRED	256 KB	Embedded Flash	No	FReD FW Storage	Embedded Interface	Hardware strapping	Not user clearable
FRU image	Non-Volatile	1	U_FRED	32 KB	Embedded SRAM	No	FRU	Embedded Interface	Hardware strapping	Not user clearable

CPLD SRAM	Non-Volatile	1	U_CPLD	64 Kb	SRAM	No	Power sequence control	I2C interface via FMP	Not accessible	Not user clearable
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