



Statement of Volatility – Dell EMC PowerEdge MX5016s

The Statement of Volatility provides you the information related to volatile and non-volatile components of different configurations of Dell EMC PowerEdge servers.


Volatile components lose their data when power cord is removed from the system, whereas, non-volatile components continue to retain their data when the power has been removed from the component.

The following table provides information of different configurations of the PowerEdge MX5016s.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write-protected?	How is memory cleared?
Expander Board										
NVSRAM memory	Non-Volatile	1	U6	1 Mb	Flash	No	FW config data	Common Flash memory Interface (CFI)	Hardware strapping	Not user clearable
Flash memory	Non-Volatile	1	U7	128 Mb	Flash	No	Firmware	Common Flash memory Interface (CFI)	Hardware strapping	Not user clearable
CPLD SRAM	Non-Volatile	1	U_CPLD	64 Kb	SRAM	No	Power sequence control	I2C interface via PM8043	Not accessible	Not user clearable
FRU image	Non-Volatile	1	U_EXP1_EEPR OM1	4 Kb	I2C EEPROM	No	FRU	I2C interface via PM8043	Hardware strapping	Not user clearable

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write-protected?	How is memory cleared?
16x2.5" Backplane										
FRU image	Non-Volatile	1	U_EEPROM	32 Kb	I2C EEPROM	No	FRU	I2C interface via PM8043	Hardware strapping	Not user clearable
CPLD SRAM	Non-Volatile	1	U_CPLD	92 Kb	SRAM	No	Power sequence control	I2C interface via PM8043	Not accessible	Not user clearable

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write- protected?	How is memory cleared?
Power BD										
FRU image	Non-Volatile	1	U_EEPROM	64 Kb	I2C EEPROM	No	FRU	I2C interface via PM8043	Hardware strapping	Not user clearable
CPLD SRAM	Non-Volatile	1	U_CPLD	64 Kb	SRAM	No	Power sequence control	I2C interface via PM8043	Not accessible	Not user clearable

 **NOTE:** For any information that you may need, direct your questions to your Dell Marketing contact.