



Statement of Volatility – Dell EMC PowerEdge FX2


Dell EMC PowerEdge FX2 contains both volatile and non-volatile (NV) components. Volatile components lose their data immediately upon removal of power from the component. Non-volatile components continue to retain their data even after the power has been removed from the component. Components chosen as user-definable configuration options (those not soldered to the motherboard) are not included in the Statement of Volatility. Configuration option information (pertinent to options such as microprocessors, remote access controllers, and storage controllers) is available by component separately. The following NV components are present in the PowerEdge FX2 server.

| Item | Non-Volatile or Volatile | Quantity | Reference Designator | Size |
|---------------------------|--------------------------|----------|----------------------|-------|
| PCIe Switch Board | | | | |
| CPLD | Non-Volatile | 1 | U_CPLD | 8Kb |
| Switch EEPROM | Non-Volatile | 2 | U2D1, U4D1 | 256Kb |
| FRU | Non-Volatile | 1 | U9E1 | 2Kb |
| Fan Only Board | | | | |
| CPLD | Non-Volatile | 1 | U_CPLD1 | 8Kb |
| FRU | Non-Volatile | 1 | U10 | 2Kb |
| 1G IO Module | | | | |
| PHY EEPROM | Non-Volatile | 1 | U_EEPROM1 | 2Kb |
| PSOC | Non-Volatile | 1 | U_PSOC1 | 32Kb |
| 10G IO Module | | | | |
| PHY EEPROM | Non-Volatile | 2 | U2E1, U2G1 | 256Kb |
| Microcontroller FW | Non-Volatile | 1 | U_ATXMEGA | 64Kb |
| PSOC | Non-Volatile | 1 | U_PSOC1 | 32Kb |
| x16 PCIe Mezzanine | | | | |
| Switch EEPROM | Non-Volatile | 1 | U1C1 | 256Kb |
| FRU | Non-Volatile | 1 | EU5C1 | 8Kb |

| Item | Type (e.g. Flash PROM, EEPROM) | Can user programs or operating system write data to it during normal operation? | Purpose? (e.g. boot code) |
|---------------------------|--------------------------------|---------------------------------------------------------------------------------|-----------------------------------|
| PCIe Switch Board | | | |
| CPLD | Integrated Flash | No | Board logic controller |
| Switch EEPROM | SPI Serial EEPROM | No | PCIe Switch configuration |
| FRU | Two-wire Serial EEPROM | No | FRU |
| Fan Only Board | | | |
| CPLD | Integrated Flash | No | Board logic controller |
| FRU | Two-wire Serial EEPROM | No | FRU |
| 1G IO Module | | | |
| PHY EEPROM | SPI Serial EEPROM | No | PHY Configuration |
| PSOC | Integrated Flash | No | Power/Reset Control |
| 10G IO Module | | | |
| PHY EEPROM | SPI Serial EEPROM | No | PHY Configuration |
| Microcontroller FW | Integrated Flash | No | Phy initialization and management |
| PSOC | Integrated Flash | No | Power/Reset Control |
| x16 PCIe Mezzanine | | | |
| Switch EEPROM | SPI Serial EEPROM | No | Switch configuration |
| FRU | Two-wire Serial EEPROM | No | FRU |

| Item | How is data input to this memory? | How is this memory write protected? |
|--------------------------|-------------------------------------------------------|-------------------------------------------------------------------|
| PCIe Switch Board | | |
| CPLD | Updated using Mainboard.bin file uploaded through CMC | Only CMC can access and uses special CRCs in bin file to validate |
| Switch EEPROM | SPI interface via CMC | Hardware Strapping |
| FRU | I2C interface via CMC | Hardware Strapping |

| Item | How is data input to this memory? | How is this memory write protected? |
|---------------------------|-------------------------------------------------------|-------------------------------------------------------------------|
| Fan Only Board | | |
| CPLD | Updated using Mainboard.bin file uploaded through CMC | Only CMC can access and uses special CRCs in bin file to validate |
| FRU | I2C interface via CMC | Hardware Strapping |
| 1G IO Module | | |
| PHY EEPROM | Flashed at Factory | Hardware Strapping |
| PSOC | Updatable through CMC serial interface | Only super-users can update. Not customer accessible |
| 10G IO Module | | |
| PHY EEPROM | Flashed at Factory | Hardware Strapping |
| Microcontroller FW | Updatable through CMC serial interface | Only super-users can update. Not customer accessible |
| PSOC | Updatable through CMC serial interface | Only super-users can update. Not customer accessible |
| x16 PCIe Mezzanine | | |
| Switch EEPROM | SPI interface via CMC | Hardware Strapping |
| FRU | I2C interface via CMC | Hardware Strapping |

 **NOTE:** For any information that you may need, direct your questions to your Dell Marketing contact.

© 2018 Dell Inc.

Trademarks used in this text: Dell™, the DELL logo, and PowerEdge™ are trademarks of Dell Inc.